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*Gen-Huey Chen; Jyr-Shiarn Yur;*
Distributed Computing Systems, 1990. Proceedings., 10th International Conference on , 28 May-1 June 1990
Page(s): 494 -501

[Abstract]   [PDF Full-Text (752 KB)] **IEEE CNF**

2 **A Petri-net-based approach to real-time program analysis**
*Buy, U.; Sloan, R.;*
Software Specification and Design, 1993., Proceedings of the Seventh International Workshop on , 6-7 Dec 1993
Page(s): 56 -60

[Abstract]   [PDF Full-Text (404 KB)] **IEEE CNF**

3 **A state-space reduction method for computing the cell loss probability in ATM networks**
*Jun Yei; Yang, T.;*
Communications, 1994. ICC 94, SUPERCOMM/ICC '94, Conference Record, Serving Humanity Through Communications. IEEE International Conference on , 1-5 May 1994
Page(s): 726 -732 vol.2

[Abstract]   [PDF Full-Text (624 KB)] **IEEE CNF**

4 **On input state space reduction and buffer noneffective region**
*Chia-Lin Hwang; San-Qi Li;*
INFOCOM '94. Networking for Global Communications. 13th Proceedings IEEE ,

12-16 June 1994
Page(s): 1018 -1028 vol.3

[Abstract] [PDF Full-Text (1016 KB)] **IEEE CNF**

---

5 **Compositional model checking of Ada tasking programs**
*Fischer, J.; Gerber, R.;*
Computer Assurance, 1994. COMPASS '94 'Safety, Reliability, Fault Tolerance, Concurrency and Real Time, Security'. Proceedings of the Ninth Annual Conference on , 27 June-1 July 1994
Page(s): 135 -147

[Abstract] [PDF Full-Text (800 KB)] **IEEE CNF**

---

6 **State space reduction in abstract interpretation of parallel programs**
*Jyh-Herng Chow; Harrison, W.L., III;*
Computer Languages, 1994., Proceedings of the 1994 International Conference on , 16-19 May 1994
Page(s): 277 -288

[Abstract] [PDF Full-Text (1036 KB)] **IEEE CNF**

---

7 **An approximation method for complete solutions of Markov-modulated fluid models**
*Qian, K.; McDonald, D.;*
Global Telecommunications Conference, 1995. GLOBECOM '95., IEEE , Volume: 2 , 13-17 Nov. 1995
Page(s): 1406 -1411 vol.2

[Abstract] [PDF Full-Text (436 KB)] **IEEE CNF**

---

8 **Designing genetic algorithms for the state assignment problem**
*Amaral, J.N.; Tumer, K.; Ghosh, J.;*
Systems, Man and Cybernetics, IEEE Transactions on , Volume: 25 Issue: 4 , April 1995
Page(s): 687 -694

[Abstract] [PDF Full-Text (704 KB)] **IEEE JNL**

---

9 **A supervisory control theoretical approach to congestion management**
*Makungu, M.; Barbeau, M.; St.-Denis, R.;*
Communication Technology Proceedings, 1996. ICCT'96., 1996 International Conference on , Volume: 2 , 5-7 May 1996
Page(s): 857 -861 vol.2

[Abstract] [PDF Full-Text (492 KB)] **IEEE CNF**

10 **Using partial orders for trace theoretic verification of asynchronous circuits**
*Yoneda, T.; Yoshikawa, T.;*
Advanced Research in Asynchronous Circuits and Systems, 1996. Proceedings., Second International Symposium on , 18-21 March 1996
Page(s): 152 -163

[Abstract]  [PDF Full-Text (940 KB)] **IEEE CNF**

11 **Evaluating deadlock detection methods for concurrent software**
*Corbett, J.C.;*
Software Engineering, IEEE Transactions on , Volume: 22 Issue: 3 , March 1996
Page(s): 161 -180

[Abstract]  [PDF Full-Text (2260 KB)] **IEEE JNL**

12 **A new finite-state vector quantizer with optimized state space and derailment-free operation**
*Ginesta, X.; Kim, S.P.;*
Image Processing, IEEE Transactions on , Volume: 5 Issue: 2 , Feb. 1996
Page(s): 383 -386

[Abstract]  [PDF Full-Text (408 KB)] **IEEE JNL**

13 **Efficient verification of real-time systems: compact data structure and state-space reduction**
*Larsen, K.G.; Larsson, F.; Pettersson, P.; Yi, W.;*
Real-Time Systems Symposium, 1997. Proceedings., The 18th IEEE , 2-5 Dec. 1997
Page(s): 14 -24

[Abstract]  [PDF Full-Text (976 KB)] **IEEE CNF**

14 **Technique and tool for symbolic representation and manipulation of stochastic transition systems**
*Siegle, M.;*
Computer Performance and Dependability Symposium, 1998. IPDS '98. Proceedings. IEEE International , 7-9 Sept. 1998
Page(s): 272

[Abstract]  [PDF Full-Text (12 KB)] **IEEE CNF**

15 **An overview and application of model reduction techniques in formal verification**
*Baumgartner, J.; Heyman, T.;*
Performance, Computing and Communications, 1998. IPCCC '98., IEEE

International , 16-18 Feb. 1998
Page(s): 165 -171

[Abstract]   [PDF Full-Text (616 KB)] **IEEE CNF**

16 **Using abstraction and model checking to detect safety violations in requirements specifications**
*Heitmeyer, C.; Kirby, J., Jr; Labaw, B.; Archer, M.; Bharadwaj, R.;*
Software Engineering, IEEE Transactions on , Volume: 24 Issue: 11 , Nov. 1998
Page(s): 927 -948

[Abstract]   [PDF Full-Text (1408 KB)] **IEEE JNL**

17 **Performance between circuit allocation schemes for half- and full-rate connections in GSM**
*Ivanovich, M.; Zukerman, M.; Fitzpatrick, P.; Gitlits, M.;*
Vehicular Technology, IEEE Transactions on , Volume: 47 Issue: 3 , Aug. 1998
Page(s): 790 -797

[Abstract]   [PDF Full-Text (164 KB)] **IEEE JNL**

18 **Ordering method for reducing state space in compositional verification**
*Wan Bok Lee; Tag Gon Kim;*
Systems, Man, and Cybernetics, 1999. IEEE SMC '99 Conference Proceedings.
1999 IEEE International Conference on , Volume: 1 , 12-15 Oct. 1999
Page(s): 806 -811 vol.1

[Abstract]   [PDF Full-Text (496 KB)] **IEEE CNF**

19 **Verification of concurrent client-server real-time scheduling systems**
*Pao-Ann Hsiung; Farn Wang; Yue-Sun Kuo;*
Real-Time Computing Systems and Applications, 1999. RTCSA '99. Sixth
International Conference on , 13-15 Dec. 1999
Page(s): 228 -235

[Abstract]   [PDF Full-Text (632 KB)] **IEEE CNF**

20 **Towards performance analysis with partially symmetrical SWN**
*Capra, L.; Dutheillet, C.; Franceschinis, G.; Ilie, J.M.;*
Modeling, Analysis and Simulation of Computer and Telecommunication Systems,
1999. Proceedings. 7th International Symposium on , 24-28 Oct. 1999
Page(s): 148 -155

[Abstract]   [PDF Full-Text (240 KB)] **IEEE CNF**

21 **Athena: a new efficient automatic checker for security protocol analysis**
*Dawn Xiaodong Song;*
Computer Security Foundations Workshop, 1999. Proceedings of the 12th IEEE ,
28-30 June 1999
Page(s): 192 -202

[Abstract]  [PDF Full-Text (212 KB)] **IEEE CNF**

22 **Abstraction from counters: an application on real-time systems**
*Logothetis, G.; Schneider, K.;*
Design, Automation and Test in Europe Conference and Exhibition 2000.
Proceedings , 27-30 March 2000
Page(s): 486 -493

[Abstract]  [PDF Full-Text (132 KB)] **IEEE CNF**

23 **Stochastic automata networks for dependability modelling**
*Stewart, W.J.; Plateau, B.;*
Aerospace Conference Proceedings, 2000 IEEE , Volume: 4 , 18-25 March 2000
Page(s): 435 -448 vol.4

[Abstract]  [PDF Full-Text (956 KB)] **IEEE CNF**

24 **Study of methods for model reduction in transition systems**
*Sarkar, S.; Subramaniam, A.V.; Neogi, R.;*
Systems, Man, and Cybernetics, 2000 IEEE International Conference on ,
Volume: 1 , 8-11 Oct. 2000
Page(s): 172 -176 vol.1

[Abstract]  [PDF Full-Text (356 KB)] **IEEE CNF**

25 **Verification of a wireless ATM medium-access protocol**
*Sidorova, N.; Steffen, M.;*
Software Engineering Conference, 2000. APSEC 2000. Proceedings. Seventh
Asia-Pacific , 5-8 Dec. 2000
Page(s): 84 -91

[Abstract]  [PDF Full-Text (748 KB)] **IEEE CNF**

26 **Model checking programs**
*Visser, W.; Havelund, K.; Brat, G.; Seungjoon Park;*
Automated Software Engineering, 2000. Proceedings ASE 2000. The Fifteenth
IEEE International Conference on , 11-15 Sept. 2000
Page(s): 3 -11

[Abstract]   [PDF Full-Text (836 KB)] **IEEE CNF**

---

27 **Exploiting heap symmetries in explicit-state model checking of software**
*Iosif, R.;*
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annual International Conference on , 26-29 Nov. 2001
Page(s): 254 -261

[Abstract]   [PDF Full-Text (803 KB)] **IEEE CNF**

---

28 **Simulation-guided property checking based on multi-valued AR-automata**
*Ruf, J.; Hoffmann, D.W.; Kropf, T.; Rosenstiel, W.;*
Design, Automation and Test in Europe, 2001. Conference and Exhibition 2001. Proceedings , 13-16 March 2001
Page(s): 742 -748

[Abstract]   [PDF Full-Text (540 KB)] **IEEE CNF**

---

29 **Reduction of the RoboCup state space**
*Liu Lin; Zheng Zhiqiang;*
Intelligent Control and Automation, 2002. Proceedings of the 4th World Congress on , Volume: 1 , 10-14 June 2002
Page(s): 479 -482 vol.1

[Abstract]   [PDF Full-Text (355 KB)] **IEEE CNF**

---

30 **A framework for optimal battery management for wireless nodes**
*Adamou, M.; Sarkar, S.;*
INFOCOM 2002. Twenty-First Annual Joint Conference of the IEEE Computer and Communications Societies. Proceedings. IEEE , Volume: 3 , 23-27 June 2002
Page(s): 1783 -1792 vol.3

[Abstract]   [PDF Full-Text (349 KB)] **IEEE CNF**

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